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App No.: 10/602,721 Inventor: Chandra Mouli, et al.

Title: REDUCED BARRIER PHOTODIODE/TRANSFER GATE DEVICE STRUCTURE FOR HIGH EFFICIENCY CHARGE TRANSFER AND REDUCED LAG AND METHOD OF FORMATION

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FIG. 1 (PRIOR ART)

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Docket No.: M4065.0761/P761

App No.: 10/602,721 Docket No.: M4065.0761/P76
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Title: REDUCED BARRIER PHOTODIODE/TRANSFER GATE
DEVICE STRUCTURE FOR HIGH EFFICIENCY CHARGE TRANSFER AND REDUCED LAG AND METHOD OF

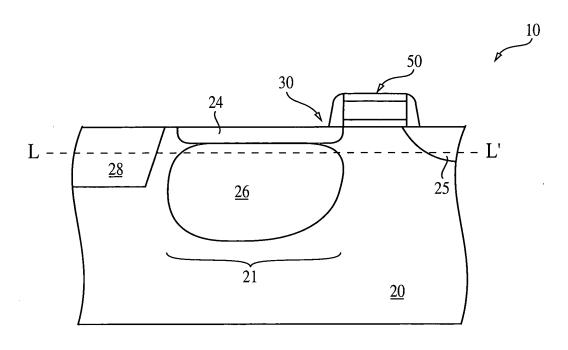


FIG. 2 (PRIOR ART)

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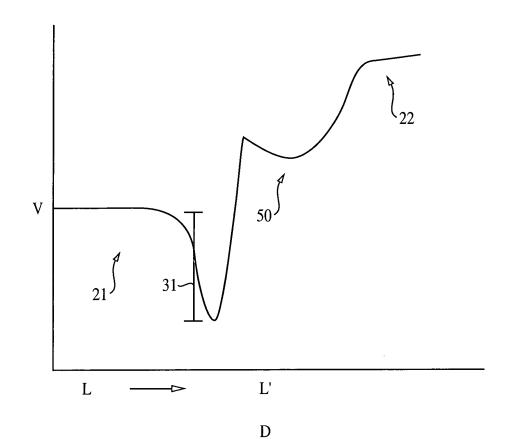


FIG. 3

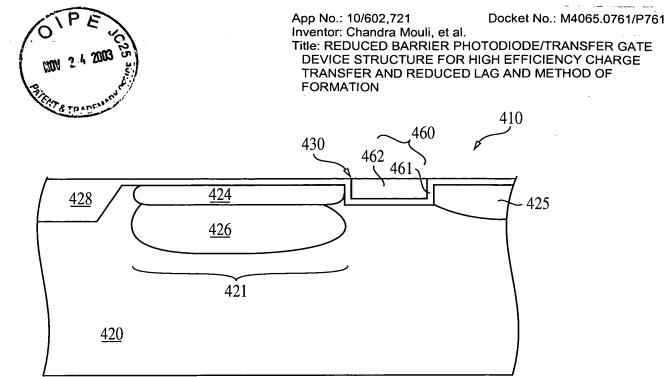


FIG. 4

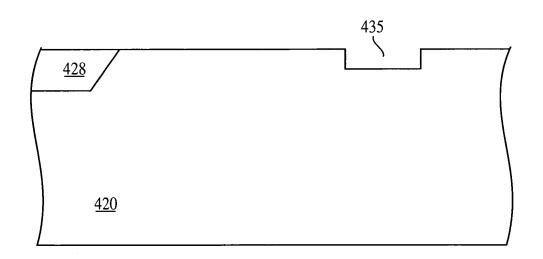


FIG. 5A



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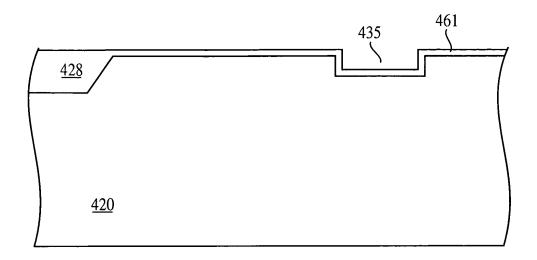


FIG. 5B

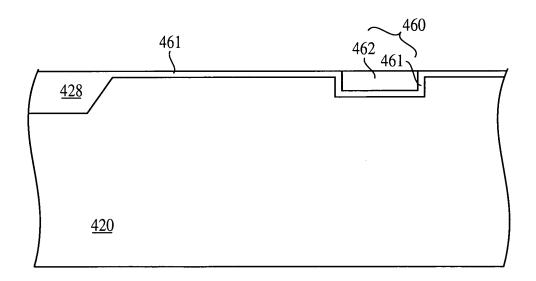


FIG. 5C

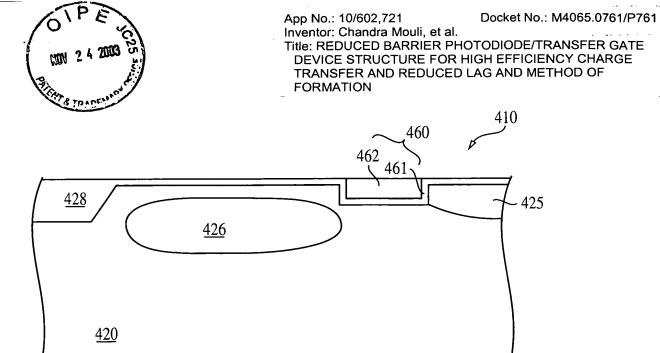


FIG. 5D

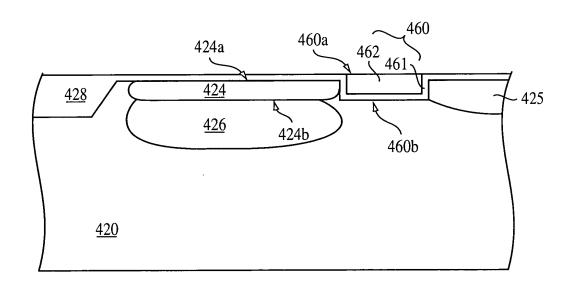


FIG. 5E

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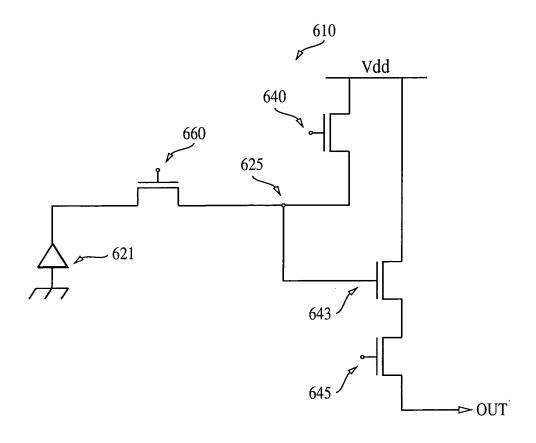


FIG. 6

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